



INDIAN INSTITUTE OF TECHNOLOGY PATNA

Bihta, Patna – 801106

Research and Development Unit

Advertisement Number: R&D/817/ULJ/368

DATED: 15.04.2024

Project Number: R&D/SP/EE/ULJ/2023-24/817

Applications are invited in the prescribed format only for the following assignment in a purely time bound research project undertaken in this institute.

1. (a) **Name of the temporary assignment** : Junior Research Fellow
- (b) **Number of Post** : One (01)
- (c) **Duration of the Post** : Initially for 1 year, may be extended for maximum 2 years or till the end of the project, whichever is earlier
- (d) **Name of the temporary research project** : Ultra low Jitter, Low Spur and Low power Frequency Synthesizers for High Speed Data Converters, Wi-Fi 7, 5G and beyond Communication Systems.
2. **Name of the sponsoring Agency** : Science and Engineering research Board (SERB)
3. **Consolidated Fellowship** : Rs. 37000/ for the first two years followed by Rs. 42,000/- for the 3rd year
4. **Qualifications & Experience** :
 - a) For candidates with M.Tech./ME/MS degree in VLSI/ Communication/ Electronics/ Instrumentation: first class (minimum 60% marks or 6.50 CPI) in M.Tech./ME/MS with GATE/NET qualifications and first class (minimum 60% marks or 6.5 CPI) in B.Tech./BE.
 - b) For candidates with B. Tech./BE in Electronics/ Electrical/ Instrumentation) as qualifying degree: 70% marks or 7.50 CPI in B.Tech./BE from institutes other than IITs/IISc and 7.0 CPI in B.Tech. from IITs and IISc.
 - c) Candidate with knowledge of designing CMOS circuits, eps PLL, and experience of using CAD tools like Cadence will be given preference.

The applicant should have qualified GATE or any other national level exam like NET.

Note: The candidate will be encouraged to apply for admission in PhD program at IIT Patna.

Interested and eligible persons may apply by emailing their application in the prescribed format along with a CV emphasizing their suitability for the project to:

Dr. Yatendra Kumar Singh, Department of Electrical Engineering, IIT Patna, Email: yatendra@iitp.ac.in Tel.: 06115-233092.

The shortlisted candidates will be informed accordingly to appear for the online interview. The incomplete application may not be considered.

Last date of receiving application: 28-Apr-2024

Online Interview Link will be intimated via email to the shortlisted candidates.

Deputy Registrar, R&D Unit.



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FORMAT OF APPLICATION FOR JRF

Name & Address Including email id and Phone no. (for Correspondence)	Category (GEN/OBC/SC/ ST/PD)	DOB dd/ mm/yy	Professional Exam. (GATE, JEE etc) & Validity
NAME IN CAPITAL Address: Phone: Email:			
Educational Qualification			
Institute/ Board	Exam Passed	Year of Passing	% of Marks/CPI
	10 th Class		
	12 th Class		
	Bachelors (B.Tech/B.E.)		
	Masters (M.Tech/M.E/MS)		

Qualifying degree	Degree/ major/Specialization
(B.Sc/B.Tech/B.E./BCA)	
(M.Sc/M.Tech/M.E/MA/MCA)	
Others	

Signature of applicant

Date:

Place: